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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10565468
	Filing Date	2006-01-19
	First Named Inventor	Qingliang Liu
	Art Unit	2416
	Examiner Name	Maxwell A. Clark
	Attorney Docket Number	80170-1010

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	1	0 428 407 A	EP	A	1991-05-22	Digital Equipment Corp.	Abstr., Pg 3, line 8-pg 4, line 5; pg 6, line 10-pg 7, line 10; pg 9, line 17-pg 12, line 15; pg 14, line 44-50; pg 15, line 32-36	<input type="checkbox"/>

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Not for submission under 37 CFR 1.99)

Application Number	10585468
Filing Date	2008-01-19
First Named Inventor	Qingliang Liu
Art Unit	2416
Examiner Name	Maxwell A. Clark
Attorney Docket Number	80170-1010

1	FAN YAXI ET AL, "Design and FPGA Implementation of Ethernet over Multiple E1 Links", Application of Electronic Techniques, vol. II, 2002, pages 72-74.	<input type="checkbox"/>
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Examiner Signature	/Brian Nguyen/	Date Considered	06/03/2009
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